IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chris D. Atkinson, et al.

Art Unit (parent case):

2812

Serial No.:

TBD

Examiner (parent case): Stevenson, Andre C.

Filed:

Herewith

Docket:

TI-31132.1

For: Error Reduction In Semiconductor

Conf. No.:

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Processes

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner For Patents Alexandria, VA 22313-1450

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. § 1.8(A) I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Alexandrja, VA 22313-1450. Elizabeth Austin

Applicant wishes to bring to the attention of the Patent and Trademark Office the information noted on the enclosed PTO-1449. This information disclosure statement is being mailed before receipt of a first office action on the merits in accordance with 37 C.F.R. 1.56, 1.97, and 1.98.

No fees are due; however, should the Office decide otherwise, please charge the necessary fee to the deposit account of Texas Instruments Incorporated, Account Number 20-0668.

Respectfully submitted,

Michael K. Skrehot

Attorney for Applicants

Reg. No. 36,682

Texas Instruments Incorporated P. O. Box 655474, MS 3999 Dallas, Texas 75265 (972) 917-5653

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Sheet	1	of	2	Attorney Docket No.	TI-31132.1	

<u> </u>	U.S. PATENT DOCUMENTS								
	U.S. Patent Document		Name of Patentee or Applicant of	Date of Pub. of					
Exam. Initials	Cite No.1	Number	Kind Code ² (if known)	Cited Doc.	Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
	AA	5,338,630		Yoon, et al.	08/16/1994				
	AB	6,219,144 B1		Hill, et al.	04/17/2001				
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Exam. Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
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